

PAAVAI ENGINEERING COLLEGE (Autonomous)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

M.E. - VLSI DESIGN

BATCH: 2015 - 2017

2.6.2-CO ATTAINMENT

Semester	Course code	Course Name	Target	CO1	CO2	CO3	CO4	CO5
I	C101	PMA15103 Applied Mathematics for Electronics Engineers	60	62.60	60.20	41.20	59.40	NA
	C102	PVL15101 Digital Signal Processing Integrated Circuits	60	72.56	70.58	65.02	75.65	71.25
	C103	PVL15102 Advanced Digital System Design	60	73.65	71.25	69.58	72.58	78.32
	C104	PVL15103 VLSI Design Techniques	60	75.65	74.26	71.32	72.65	71.32
	C105	PVL15104 Solid State Device Modeling and Simulation	60	76.35	74.25	68.32	68.45	NA
	C106	PVL15151 Low Power VLSI Design	60	76.25	74.35	68.45	69.27	68.45
	C107	PVL15105 VLSI Design Lab I	60	84.26	80.25	73.68	77.26	NA
II	C108	PVL15201 Analysis and Design of Analog Integrated Circuits	60	73.25	78.35	77.26	74.25	76.81
	C109	PVL15202 Computer Aided Design of VLSI Circuits	60	76.81	78.25	74.65	77.26	74.25
	C110	PVL15203 VLSI Signal Processing	60	74.25	78.00	76.21	NA	NA
	C111	PVL15251 CMOS VLSI Design	60	81.20	78.65	77.67	76.25	73.54
	C112	PVL15352 Advanced Microprocessors and Microcontrollers	60	82.32	78.65	73.54	72.36	NA
	C113	PVL15454 VLSI For Wireless Communication	60	78.23	75.65	79.23	75.65	NA
	C114	PVL15204 VLSI Design Lab II	60	82.92	83.56	84.25	84.94	NA
III	C201	PVL15301 Testing of VLSI Circuits	60	80.00	79.00	78.50	79.00	78.50
	C202	PVL15553 Embedded Systems	60	80.23	79.36	72.01	74.36	72.01
	C203	PVL15651 Computer Architecture and Parallel Processing	60	78.63	77.32	75.12	74.35	NA
	C204	PVL15302 Project Work (Phase I)	60	76.35	75.69	72.36	70.00	78.26
IV	C205	PVL15401 Project Work (Phase II)	60	84.12	83.25	80.00	85.36	82.21


 HEAD OF THE DEPARTMENT
 Electronics & Communication Engg.
 PAAVAI ENGINEERING COLLEGE
 NH-7, PACHAL (Po) Namakkal - 637 018


 PRINCIPAL
 PAAVAI ENGINEERING COLLEGE
 NH-7, PACHAL Post, NAMAKKAL Dist